



IFW

XA-9890
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Takashi MIWA et al.

Appln. No.: 10/606,891

Group Art Unit: 2824

Filed: June 27, 2003

Examiner: A. Tran

For: STACKED LARGE-SCALE INTEGRATED CIRCUIT (LSI)
SEMICONDUCTOR DEVICE WITH MINIATURIZATION AND THINNING OF
PACKAGE (AS AMENDED)

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

It is respectfully requested that the time now set for response be extended, retroactively, by one month. If any further extension of time is required to avoid abandonment of this application, such extension is hereby requested. The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper to Deposit Account No. 50-1165.

Respectfully submitted,

05/16/2005 RMEBRAHT 00000025 501165 10606891

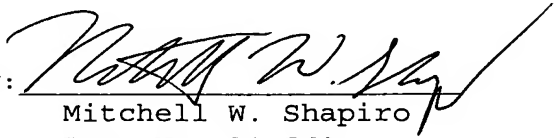
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MWS:kss

Miles & Stockbridge P.C.
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May 11, 2005

By:


Mitchell W. Shapiro
Reg. No. 31,568

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.

5/11/05
Date


Signature